

WRITE POINTER ERROR RECOVERY

ABSTRACT OF THE DISCLOSURE

5 A write pointer (21) from a write pointer circuit (13) may cause a demultiplexer circuit (12) to direct data from a memory cell (11A-11N) to a desired bit location (0-4) in a register 14. A read pointer (20) may cause a multiplexer circuit (15) to select data from a desired bit location in the register to provide as output data (19) or to select one of the bits of the write pointer. The write pointer may be incremented by a data strobe signal (17). The state of the write pointer may be
10 determined by reading the bits of the write pointer, and the write pointer may be synchronized via a reset line (18).